

ME VLSI Design

Semester I

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|---|-----------------------|
| 1 | Advanced Mathematics |
| 2 | VLSI Design |
| 3 | Digital System Design |

Elective I

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| 1 | Low Power VLSI Design |
| 1 | Micro Electro Mechanical Systems |
| 2 | Microelectronics |
| 3 | VLSI Physical Design Lab |

Semester II

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| 1 | Digital Signal Processing |
| 2 | Analog & Mixed Signal ICs |
| 3 | CAD of ICs |

Elective II

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| 1 | High Level System Design & Modeling |
| 2 | FPGA Based System Design |
| 3 | Biomedical Electronics |
| 4 | Digital System Design Lab |

Semester III

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| 1 | Embedded System Design 3 1 25 100 125 |
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Elective III

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| 1 | VLSI Testing & Testability |
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- 2 Nano-electronics
- 3 Advance IC Technology and Characterization techniques
- 4 Dissertation (Stage-I)
- 5 Seminar

Seminar IV

- 1 Dissertation (Stage-II)